INPUT

```
=====
pins          rectlog stage     output stage
SIG_IN –> RECTLOG_IN  –> OUTPUT_IN –> VU-
SIGGND –v
DCGND  –> RECTLOG_GND –> OUTPUT_GND
DC12V  –> VU+
=====
```

RECTLOG

```
B=diode BAT85
D=diode 1N4148
P=path
======
IN  –> –[ B ]|––[10r]– –– –– –> OUTPUT_IN
                       |  |  |
                       P  P  P
                       1  2  3
                       |  |  |
GND –> ––––––––––––––– –– –– –> OUTPUT_GND
======
   P1 –> ––[+ 0.1uF -]––––––––– –> GND
   P2 –> ––[12K]––[D]|––––––––– –> GND
   P3 –> ––[27K]––[D]|––[D]|–– –> GND
======
```

OUTPUT

```
Q=BC457c
P=10K lin
M=VUmeter
======
          ____
         |   v
IN  –> ––– –[ P ]–––
                    |2b
                  [ Q ]
                3e/  \1c
GND –> ––––––––––       ––––
                              |
12V –> ––––––[ + M - ]–––––––
======
```